

Refine Search

Search Results -

Terms	Documents
L1 same multiple same (empt\$3 or free)	5

Database:

US Pre-Grant Publication Full-Text Database

US Patents Full-Text Database

US OCR Full-Text Database

EPO Abstracts Database

JPO Abstracts Database

Derwent World Patents Index

IBM Technical Disclosure Bulletins

Search:

L2

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Wednesday, April 12, 2006 [Printable Copy](#) [Create Case](#)**Set Name Query**

side by side

DB=PGPB; PLUR=YES; OP=OR

Hit Count Set Name

result set

L2 L1 same multiple same (empt\$3 or free) 5 L2L1 synchronous\$3 same asynchronous\$3 same (rate or speed) 1790 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L4 and bridge	0

Database:

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 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
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 Derwent World Patents Index
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Search:

L6

Refine Search

Recall Text 

Clear

Interrupt

Search History

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Set Name Query

side by side

Hit Count Set Name

result set

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L6 L4 and bridge

0 L6

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L5 L4 and bridge

31 L5

L4 L1 same multiple

231 L4

L3 L1 same multiple same (empt\$3 or free)

5 L3

DB=PGPB; PLUR=YES; OP=OR

L2 L1 same multiple same (empt\$3 or free)

5 L2

L1 synchronous\$3 same asynchronous\$3 same (rate or speed)

1790 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(375/354 370/230 370/232 370/235 370/401 370/402 709/253 710/306 710/310 710/311 710/52 710/53 710/100 710/33 710/307 340/2.1 712/29 713/322 713/501 713/600).ccls.	16596

Database:

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 US OCR Full-Text Database
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Search:

L7

Refine Search

Recall Text

Clear

Interrupt

Search History

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Set

Name Query

side by side

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L7 710/306,310,311,52,53,100,33,307;713/322,501,600;712/29;709/253;375/354;370/230,232,235,401,402;340/2.1

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L6 L4 and bridge

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L5 L4 and bridge

L4 L1 same multiple

L3 L1 same multiple same (empt\$3 or free)

DB=PGPB; PLUR=YES; OP=OR

L2 L1 same multiple same (empt\$3 or free)

L1 synchronous\$3 same asynchronous\$3 same (rate or speed)

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L5 and L7	2

Database:

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Search:

L8

Refine Search

Recall Text

Clear

Interrupt

Search History

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SetName Query

side by side

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L8 L5 and L7L7 710/306,310,311,52,53,100,33,307;713/322,501,600;712/29;709/253;375/354;370/230,232,235,401,402;340/2.1

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L6 L4 and bridge

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L5 L4 and bridgeL4 L1 same multipleL3 L1 same multiple same (empt\$3 or free)

DB=PGPB; PLUR=YES; OP=OR

L2 L1 same multiple same (empt\$3 or free)L1 synchronous\$3 same asynchronous\$3 same (rate or speed)

END OF SEARCH HISTORY

EAST - [Untitled1:1]

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☐ Drafts
☐ Pending
☒ **Active**
☐ L1: (4326) synchronous\$
☐ L2: (5) 11 same multipl
☐ Failed
☐ Saved
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☐ Tagged (0)
☐ UDC
☐ Queue
☐ Trash

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 Default operator: OR

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BRS form IS&R form Image Text HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comment	Error	Definit	Er
1	BRS	L1	4326	synchronous\$3 same asynchronous\$3 same (USPA T	2006/04/1 2 16:35				
2	BRS	L2	5	11 same multiple same empt\$3	USPA T	2006/04/1 2 16:36				



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Results for "(synchronous* and asynchronous*<in>metadata) and multiple and speed and empt"

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IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

- ☐ 1. **The desk area network**
 McAuley, D.R.; Leslie, I.M.;
[Electronics & Communication Engineering Journal](#)
 Volume 9, Issue 3, June 1997 Page(s):143 - 152
[AbstractPlus](#) | Full Text: [PDF](#)(1592 KB) [IEEE JNL](#)
- ☐ 2. **Memory faults in asynchronous microprocessors**
 Lloyd, D.W.; Garside, J.D.; Gilbert, D.A.;
[Advanced Research in Asynchronous Circuits and Systems, 1999. Proceedings. Fifth International Symposium on](#)
 19-21 April 1999 Page(s):71 - 80
 Digital Object Identifier 10.1109/ASYNC.1999.761524
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Memory faults in asynchronous microprocessors

[Lloyd, D.W.](#) [Garside, J.D.](#) [Gilbert, D.A.](#)

Dept. of Comput. Sci., Manchester Univ., UK;

This paper appears in: [Advanced Research in Asynchronous Circuits and Systems, 1999. Proceedings., Fifth International Symposium on](#)

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Abstract

Although a large number of asynchronous microprocessors have now been designed, relatively few have attempted to handle memory faults. Memory faults create problems for the design of any pipelined system which are exacerbated by the non-deterministic nature of an asynchronous processor. This paper describes these problems as encountered in the design of asynchronous ARM processors and discusses their specific solutions in the AMULET3 processor. Different mechanisms were used, as expedient, to maintain coherency for the various state-holding elements within the processor; these include register renaming and history buffering in addition to resource locking

Index Terms

Inspec

Controlled Indexing

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Non-controlled Indexing

[AMULET3 processor](#) [ARM processors](#) [asynchronous microprocessors](#) [history buffering](#) [memory faults](#) [pipelined system](#) [register renaming](#) [resource locking](#)

Author Keywords

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References

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